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By: Werner Stemmer

Date: April 28, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/706,841 Confirmation No: 6926
Applicant : Franz Hofmann et al.
Filed : November 12, 2003
Title : Method for Manufacturing a Multi-Bit Memory Cell
Art Unit : 2818
Examiner : Quoc Dinh Hoang

Docket No. : P2000,0149 D
Customer No. : 24131

R E S P O N S E

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated April 19, 2005, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group II, claims 1-3, for prosecution at this time.

Applicants reserve the right to file a divisional application including the device claim of group I at a later date.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-3 is solicited.

Respectfully submitted,

Werner Stemmer
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Date: April 28, 2005

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